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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/724,597	11/28/2000	Peter L. Rosefield	ATI010002	7653	
, 75	90 06/05/2002				
J. Gustav Larson Silnon, Fakhoury, Tangalos, Frantz & Galasso, PLC P.O. Box 26503			EXAMINER		
			SOWARD, IDA M		
Austin, TX 78	755-0503		ART UNIT PAPER NUMBER		
			2822		
			DATE MAILED: 06/05/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
•				1			
	Office Action Summers	09/724,597	ROSEFIELD ET AL.				
	Office Action Summary	Examiner	Art Unit				
	· The MAN INC DATE of this control is also	Ida M Soward	2822				
Period	The MAILING DATE of this communication app for Reply	pears on the cover sheet with	the correspondence address				
THE - Ex aft - If t - If t - Fa - An	HORTENED STATUTORY PERIOD FOR REPLE MAILING DATE OF THIS COMMUNICATION. tensions of time may be available under the provisions of 37 CFR 1.1 er SIX (6) MONTHS from the mailing date of this communication. he period for reply specified above is less than thirty (30) days, a reply operiod for reply is specified above, the maximum statutory period filture to reply within the set or extended period for reply will, by statute y reply received by the Office later than three months after the mailing med patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a repl by within the statutory minimum of thirty (will apply and will expire SIX (6) MONTH e, cause the application to become ABAN	y be timely filed 30) days will be considered timely. IS from the mailing date of this communication IDONED (35 U.S.C. § 133).	on.			
1)[>	Responsive to communication(s) filed on 15.	April 2002 .					
2a)[nis action is non-final.					
3)[, -		rs, prosecution as to the merits	is			
•	closed in accordance with the practice under ition of Claims						
· · _	Claim(s) <u>1-20</u> is/are pending in the application	า					
7/2	4a) Of the above claim(s) <u>1-13</u> is/are withdrawi						
5)[Claim(s) is/are allowed.						
-	Claim(s) 14-20 is/are rejected.						
•	Claim(s) is/are objected to.						
8)[Claim(s) are subject to restriction and/o	or election requirement.					
Applica	tion Papers						
-	The specification is objected to by the Examine						
10)[The drawing(s) filed on is/are: a)☐ acce	pted or b) objected to by the	Examiner.				
—	Applicant may not request that any objection to th						
11)_	The proposed drawing correction filed on		approved by the Examiner.				
40)	If approved, corrected drawings are required in re	•					
. —	The oath or declaration is objected to by the Ex	aminer.					
-	under 35 U.S.C. §§ 119 and 120		140() ()) (()				
-	Acknowledgment is made of a claim for foreign	n priority under 35 U.S.C. §	119(a)-(d) or (t).				
a	ı)						
	1. Certified copies of the priority document		thankan Ma				
	2. Certified copies of the priority document						
	3. Copies of the certified copies of the prio application from the International Bu See the attached detailed Office action for a list	reau (PCT Rule 17.2(a)).					
14)	Acknowledgment is made of a claim for domesti	ic priority under 35 U.S.C. §	119(e) (to a provisional applicat	ion).			
	a) The translation of the foreign language pro Acknowledgment is made of a claim for domest	' '					
Attachme	ont(s)						
2) 🔲 No	tice of References Cited (PTO-892) cice of Draftsperson's Patent Drawing Review (PTO-948) commation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Info	mmary (PTO-413) Paper No(s) ormal Patent Application (PTO-152)				

Application/Control Number: 09/724,597

Art Unit: 2822

DETAILED ACTION

This office action is in response to the election filed on 4-15-02.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 14 and 17-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior Art Figures 1-3 in view of Kameda et al. (6,130,484).

Prior Art Figures 1-3 teach an apparatus comprising: semiconductor substrate having an input output (I0) ring, the I0 ring having a bond pad portion and an active buffer portion; the bond pad portion including: a first bond pad; a second set of bond pads having one or more bond pads; and a third bond pad, wherein the second set of bond pads is immediately adjacent to the first and third bond pads. Prior Art Figures 1-3 further teach the second set of bond pads including one or more bond pads. However, Prior Art Figures 1-3 fail to teach a conductive trace coupling the first bond pad to the third bond pad. Kameda et al. teach a conductive trace 3a coupling a first bond pad 5 to a third bond pad 11b (Figures 1-2, col. 3, lines 37-65). Kameda et al. further teaches a package substrate having a power portion; a first bond wire 7 connected to the first bond pad 5 and the power portion; a second bond wire 3a connected to the third bond

Application/Control Number: 09/724,597

Art Unit: 2822

pad **11b** and the power portion, wherein one of the first bond pad and the third bond pad being connected to the active buffer portion **2** of the I0 ring. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the structure of Prior Art Figures 1-3 with the conductive trace of Kameda et al. to improve the reliability of the integrated circuit IC.

Claims 15-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior Art Figures 1-3 and Kameda et al. (6,130,484) as applied to claim 14 above, and further in view of Lebby et al. (5,5543,958) and Ngai et al. (US 2001/0010471 A1). Kameda et al. further teach a first bond pad 5 and a third bond pad 4 being power pads (Figures 1-2, col. 3, lines 37-65). However, Prior Art Figures 1-3 and Kameda et al. fail to teach a power pad coupled to a fixed voltage source, wherein the fixed voltage source is one of Vdd and Vss. Lebby et al. teach a power pad 27 coupled to a fixed voltage source (Figure 1, col. 4, lines 42-46). Ngai et al. teach a fixed voltage source is one of Vdd and Vss (page 5, paragraph [0062]). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the structure of Prior Art Figures 1-3 and the conductive trace of Kameda et al. to improve the reliability of the integrated circuit IC with the power pad of Lebby et al. and the fixed voltage source of Ngai et al. to provide high performance interconnects.

Application/Control Number: 09/724,597

Art Unit: 2822

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

The following patents have been cited to further show the state of the art with respects to semiconductor substrate having an input output (I0) ring:

Ibnabdeljalil et al. (US 6,365,978 B1)

Ker et al. (US 2001/0010408 A1)

Page 4

Kohara et al. (US 2001/0011768 A1)

Yoshida (US 2001/0011771 A1).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ida M Soward whose telephone number is 703-305-3308. The examiner can normally be reached on Monday - Friday, 7:30 am to 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on 703-308-4940. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

CARL WHITEHEAD, J.P.
UPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2800

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May 24, 2002

Page 5